Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A memory device comprising:

a memory including a plurality of low-latency, rewritable, non-volatile memory cells;

a profile storage unit including access information allocated to at least one request profile, said request profile including at least one set of request information elements including information based on requests for access to said memory;

an access control unit connected with said profile storage unit and said memory, said access control unit configured to ascertain a request profile to an access request using request information of said access request, said access control unit further configured to determine access rights of said access request in dependence on the access information allocated to the request profile of the access request.

2. (currently amended) The memory device of claim 1, wherein ~~said request profile includes at least one set of request information elements,~~ said set of request information elements ~~including~~ includes at least one request information element indicating at least one of: a type of request, an external memory client from which the request originates, a memory section the request is directed to, an access authorization, a password, a request protocol type, a time of request, an interface receiving the request, the length of the request, time span lapsed since a last request, a security class, or a priority class.

3. (previously presented) The memory device of claim 1, further comprising a plurality of interfaces for communication with external memory clients and/or for communication according to different memory uses, said interfaces being connected with said access control unit and each of said interfaces being allocated to a set of request profiles.

4. (previously presented) The memory device of claim 3, wherein at least one of said interfaces is implemented in the form of hardware.

5. (previously presented) The memory device of claim 3, wherein at least one of said interfaces is implemented in the form of software.

6. (previously presented) The memory device of claim 4, further comprising an SRAM-type interface adapted to serve separate connections for address data input and user data exchange, respectively, between the memory device and at least one external memory client.

7. (previously presented) The memory device of claim 5, further comprising an I/O-type interface adapted to serve a shared connection for address data input and user data exchange between the memory device and at least one external memory client.

8. (previously presented) The memory device of claim 1, further comprising a supervisor interface adapted to create or change at least one request profile and/or access information allocated thereto, given a predetermined condition.

9. (original) The memory device of claim 8, wherein said supervisor interface is adapted to admit or reject external requests for change of a request profile, depending on access information allocated to at least one predetermined change request profile.

10. (previously presented) The memory device of claim 3, wherein said profile storage unit comprises a set of access flags, each access flag allocated to a respective request profile, and wherein said access information is given by one of two possible states of an access flag.

11. (previously presented) The memory device of claim 1, wherein said profile storage unit is integrated into said access control unit.

12. (previously presented) The memory device of claim 1, wherein said access control unit is adapted to maintain a current copy of said profile storage unit in a predetermined section of said memory.

13. (previously presented) The memory device of claim 1, further comprising a translation unit adapted to translate between one or more different ways of memory addressing.

14. (currently amended) A method comprising:

receiving an access request to a memory, said access request including request information;

~~determining~~ ascertaining a request profile to the access request using at least some of said request information, said request profile including request information elements based on the access request;

determining access rights of said access request to said memory, said access rights based on access information allocated to said request profile.